

FIG.1

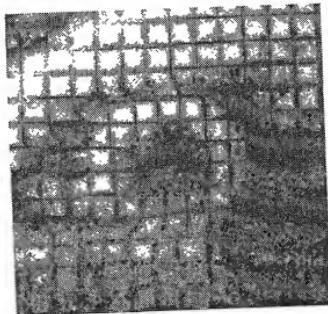


FIG.2

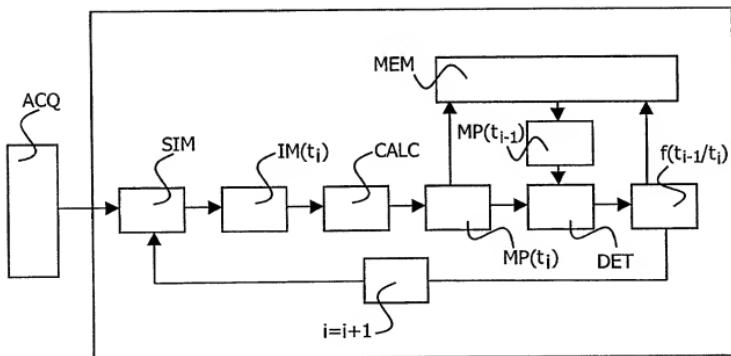


FIG.3

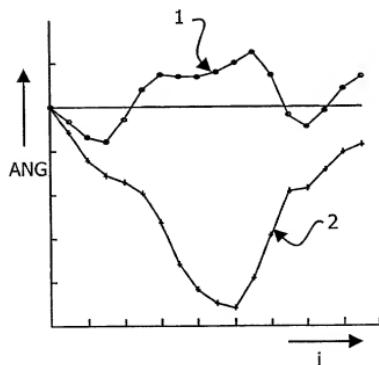


FIG.4a

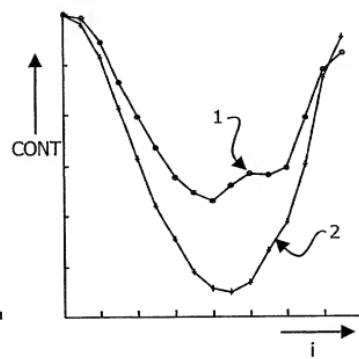


FIG.4b

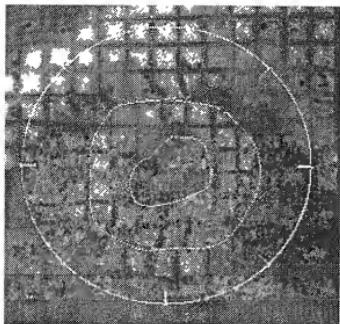


FIG.5a

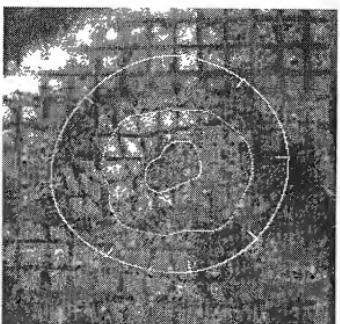


FIG.5b

ACQUISITION - PROCESSOR

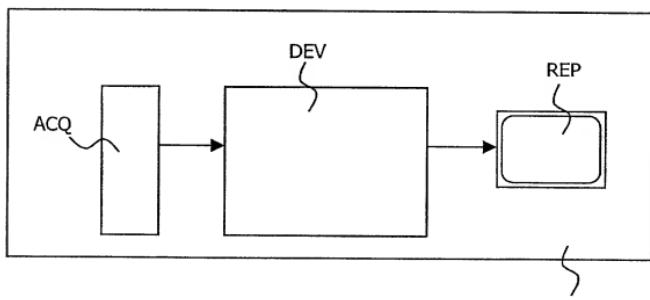


FIG.6